



DOCKET NO. S. KAPUR 5-10

PATENT

123/figs
2-18-04
H all

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Sharad Kapur, *et al.*

Serial No.: 09/427,238

Filed: October 26, 1999

For: SYSTEM AND METHOD FOR DETERMINING CAPACITANCE
FOR LARGE-SCALE INTEGRATED CIRCUITS

Group No.: 2123

Examiner: Eduardo Garcia-Otero

RECEIVED

FEB 11 2004

Technology Center 2100

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313, on
02/04/2004 (Date)
Stephanie Pitt
(Printed or typed name of person signing the certificate)
Sharad Kapur
(Signature of the person signing the certificate)

Sir:

PRELIMINARY AMENDMENT

Before examination of the above-identified application, please consider the substitute application, the replacement drawing sheets and the following remarks/arguments.